		Application/Control No. 10/083,224	Applicant(s)/Patent Under Reexamination O'BRIEN ET AL.	
Notice of References Cited		Examiner John J. Tabone, Jr.	Art Unit 2133	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,434,804 A	07-1995	Bock et al.	702/117
	B	US-6,446,230 B1	09-2002	Chung, Sung Soo	714/726
	C	US-6,055,649	04-2000	Deao et al.	714/30
	D	US-6,055,649 A	04-2000	Deao et al.	714/30
	E	US-6,381,717 B1	04-2002	Bhattacharya, Debashis	714/724
	F	US-6,775,793 B2	08-2004	Deao et al.	714/29
	G	US-5,768,152 A	06-1998	Battaline et al.	702/186
	H	US-6,289,300 B1	09-2001	Brannick et al.	703/28
	I	US-2001/0010083	07-2001	SATOH, SHUJI	714/30
	J	US-6,408,413 B1	06-2002	Whetsel, Lee D.	714/727
	K	US-2002/0002691	01-2002	Whetsel, Lee D.	714/28
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Winters, M, Using IEEE-1149.1 For In-Circuit Emulation, September 27-29 1994, WESCON/94. 'Ideal/Microelectronics' Conference Record, Pages 525-528.
	V	Brokish, Charles W., Emulation Fundamentals for TI's DSP Solutions, February 2001, Texas Instruments, Application Report SPRA439A
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.